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Substitute for form 1449/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>		Application Number	Unknown
		Filing Date	Herewith
		First Named Inventor	Curtis R. Priem
		Art Unit	Unknown
		Examiner Name	Unassigned
Sheet 1 of 2	Attorney Docket Number	NVDA P000455	

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
TJC		US- 5469571	11-21-1995	Bunnell	
		US- 5721922	02-24-1998	Dingwall	
		US- 5903752	05-11-1999	Dingwall et al.	
		US- 6085215	07-04-2000	Ramakrishnan et al.	
		US- 6341347	01-22-2002	Joy et al.	
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FOREIGN PATENT DOCUMENTS						
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		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
TJC		Buchanan, Matt, "Coordinated Thread Scheduling for Workstation Clusters Under Windows NT", USENIX Windows NT Workshop, 1997.	
		Denham, Jeffrey M., "DEC OSF/1 Symmetric Multiprocessing", <u>Digital Technical Journal</u> , Volume 6, Number 3.	
		Jones, Michael B., "The Problems You're Having May Not Be the Problems You Think You're Having: Results from a Latency Study of Windows NT", Microsoft Research, Microsoft Corporation, Technical Report MSR-TR-98-29, July 1998, expanded March 1999.	
		Smith, Dean, "Interrupt Handler for the IDT79R3051 Risccontroller Family", Application Note AN-131, pp. 103-107, Integrated Device Technology, 1996.	
		"Windows NT Thread Latency Measurements. Measured Response Times for the REAL_TIME_PRIORITY_CLASS", RadiSys, December 1998.	
		"Chapter 9 - Interrupts BIOS/DOS/Hardware - Direct Hardware Access", < <a href="http://homepages.lus.edu/RWISMAN/C335/HTML/Chapter9.htm">http://homepages.lus.edu/RWISMAN/C335/HTML/Chapter9.htm</a> >.	
		"The Neutrino Microkernel", < <a href="http://www.qnx.com/developer/docs/qnx_6.1_docs/neutrino/sys_arch/kernel.html">http://www.qnx.com/developer/docs/qnx_6.1_docs/neutrino/sys_arch/kernel.html</a> >.	
		Kleidermacher, David, "Safety-Critical Operating Systems," August 31, 2001, < <a href="http://www.embedded.com/story/OEG20010829S0055">http://www.embedded.com/story/OEG20010829S0055</a> >.	
↓		"Chapter 4 - Development Environment Notes," <u>Release Notes</u> , Digital UNIX Version T4.0-2, January 1996, < <a href="http://www.iagu.net/docs/dec/HTML/AA-Q0U4L-TET1_html/relnotes5.html">http://www.iagu.net/docs/dec/HTML/AA-Q0U4L-TET1_html/relnotes5.html</a> >.	

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## U. S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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		"The Neutrino Microkernel", < <a href="http://www.qnx.com/developer/docs/qnx_6.1_docs/neutrino/sys_arch/kernel.html">http://www.qnx.com/developer/docs/qnx_6.1_docs/neutrino/sys_arch/kernel.html</a> >.	
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↓		"Chapter 4 - Development Environment Notes," <u>Release Notes</u> , Digital UNIX Version T4.0-2, January 1996, < <a href="http://www.iagu.net/docs/dec/HTML/AA-Q0U4L-TET1_html/relnotes5.html">http://www.iagu.net/docs/dec/HTML/AA-Q0U4L-TET1_html/relnotes5.html</a> >.	

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